

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventorship.....Horowitz et al.
 ApplicantRambus, Inc.
 Attorney's Docket No.RB1-011US
 Title: Method and Apparatus for Multi-Level Signaling

INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

REMARKS

The citations listed, copies attached, are submitted in compliance with the duty of disclosure defined in 37 CFR §1.56. The Examiner is requested to make these citations of official record in this application.

Respectfully Submitted,

Date: 11-16-2001

By: Steven R. Sponseller

Steven R. Sponseller
 Reg. No. 39,384



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U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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				Application Number	
				Filing Date	
				First Named Inventor	Horowitz
				Group Art Unit	
				Examiner Name	
Sheet	1	of	2	Attorney Docket Number	RB1-011US

857 U.S. PTO
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11/16/01

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Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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		Application Number	
		Filing Date	
		First Named Inventor	Horowitz
		Group Art Unit	
		Examiner Name	
		Attorney Docket Number	RB1-011US

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"Coding a Terminated Bus for Low Power", M.R. Stan & W.P. Burleson, Great Lakes Symposium on VSLI, pp. 70-73, March 1995	
		"Bus-Invert Coding for Low Power I/O", M.R. Stan & W.P. Burleson, IEEE Transactions on Very Large Scale Integration Systems, Vol. XX, No. Y, 1999	

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